IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please AMEND claims 1, 6 and 11 in accordance with the following:

1.   (CURRENTLY AMENDED) A microcomputer with a debug supporting function in which a program to be executed by a CPU is ~~debug~~ debugged using an in-circuit emulator, the microcomputer comprising:

a debug target circuit ~~which has~~ having the CPU as a component and in which supply and stop of drive power can be arbitrarily switched; and

a debugging circuit which has an interface module to the in-circuit emulator and which holds a debug related setting by drive power supplied thereto, independently of the debug target circuit, in a condition in which drive power ~~supply~~ to the debug target circuit is stopped.

2.   (ORIGINAL) The microcomputer according to claim 1, further comprising a clip circuit which clips a supply signal from the debug target circuit to the debugging circuit when supply of drive power to the debug target circuit is stopped.

3.   (CURRENTLY AMENDED) The microcomputer according to claim 2, further comprising a clip control terminal to which is supplied, from outside, ~~with~~ a control signal to switch the clip circuit in accordance with, selectively, ~~whether to~~ directly ~~supply~~ supplying an output signal of the debug target circuit to the debugging circuit or ~~to clip~~ clipping the output signal of the debug target circuit.

4.   (CURRENTLY AMENDED) The microcomputer according to claim 2, wherein the clip circuit is switched ~~whether~~, selectively, to directly supply an output signal of the debug target circuit to the debugging circuit or to clip the output signal of the debug target circuit, in response to drive power supplied to the debug target circuit.

5.   (ORIGINAL) The microcomputer according to claim 1, wherein the microcomputer is a microcontroller or a microprosessor.

6.   (CURRENTLY AMENDED) A microcomputer with a debug supporting function in which a program to be executed by a CPU is ~~debug~~ debugged using an in-circuit emulator, the microcomputer comprising:

a debug target circuit ~~which has~~ having the CPU as a component;

a debugging circuit which has an interface module to the in-circuit emulator;

a first power supply terminal which supplies an external drive power to the debug target circuit; and

a second power supply terminal which supplies an external drive power to the debugging circuit, ~~independent~~ independently of power supply to the debug target circuit.

7.   (ORIGINAL) The microcomputer according to claim 6, further comprising a clip circuit which clips a supply signal from the debug target circuit to the debugging circuit when supply of drive power to the debug target circuit is stopped.

8.   (CURRENTLY AMENDED) The microcomputer according to claim 7, further comprising a clip control terminal to which is supplied, from outside, ~~with~~ a control signal to switch the clip circuit in accordance with, ~~whether to~~ selectively, directly ~~supply~~ supplying an output signal of the debug target circuit to the debugging circuit or ~~to clip~~ clipping the output signal of the debug target circuit.

9.   (CURRENTLY AMENDED) The microcomputer according to claim 7, wherein the clip circuit is switched ~~whether~~, selectively, to directly supply an output signal of the debug target circuit to the debugging circuit or to clip the output signal of the debug target circuit, in response to drive power supplied to the debug target circuit.

10.   (ORIGINAL) The microcomputer according to claim 6, wherein the microcomputer is a microcontroller or a microprosessor.

11.   (CURRENTLY AMENDED) A microcomputer with a debug supporting function in which a program to be executed by a CPU is ~~debug~~ debugged using an in-circuit emulator, the microcomputer comprising:

a debug target circuit ~~which has~~ having the CPU as a component;

a debugging circuit which has an interface module to the in-circuit emulator;

a power supply terminal which supplies an external drive power to the debugging circuit;

a switching element which switches supply and stop of the external drive power supplied through the power supply terminal to the debug target circuit; and

a switch control terminal to which a control signal for controlling the switching of the switching element is supplied from outside.

12.   (ORIGINAL) The microcomputer according to claim 11, further comprising a clip circuit which clips a supply signal from the debug target circuit to the debugging circuit when supply of drive power to the debug target circuit is stopped.

13.   (CURRENTLY AMENDED) The microcomputer according to claim 12, further comprising with a clip control terminal to which is supplied, from outside, with a control signal to switch the clip circuit whether to, selectively, directly supply supplying an output signal of the debug target circuit to the debugging circuit or to clip clipping the output signal of the debug target circuit.

14.   (CURRENTLY AMENDED) The microcomputer according to claim 12, wherein the clip circuit is switched whether, selectively, to directly supply an output signal of the debug target circuit to the debugging circuit or to clip the output signal of the debug target circuit, in response to drive power supplied to the debug target circuit.

15.   (ORIGINAL) The microcomputer according to claim 11, wherein the microcomputer is a microcontroller or a microprosessor.